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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Attention: Examiner C. T. L. Tu
Art Unit: 2133

Re: M. NAKAO, et al. - Application No.: 09/811,435
Our Ref. No.: 520.39869X00

SUBMISSION OF DOCUMENTS

Sir:

Applicants hereby submit the attached "Amendment" (12 pgs), "Extension of Time" (1 pg) and Credit Card Payment Form (1 pg) in the above-identified application.

CERTIFICATE OF TRANSMISSION:

I hereby certify that the attached "Amendment" (12 pgs), "Extension of Time" (1 pg) and Credit Card Payment Form (1 pg), are being formally filed via the USPTO Central Fax No. 703-872-9306 on May 4, 2004.


Deborah L. Therrien

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. NAKAO, et al.
Application No.: 09/811,435
Filed: March 20, 2001
For: TEST METHOD OF SEMICONDUCTOR INTEGRATED
CIRCUIT AND TEST PATTERN GENERATOR
Art Unit: 2133
Examiner: C. T. L. Tu

AMENDMENT

May 4, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated December 4, 2003, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims and

Remarks are included following the amendments.